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SERIAL NUMBER
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GROUP
2186

U.S. Patent Documents

DOCUMENT NUMBER	DATE	Name	CLASS	SUB- CLASS	FILING DATE
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	NUMBER	NUMBER	NUMBER	NUMBER	NUMBER CLASS

Foreign Patent Documents

Examiner	DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB/	TRANSLATION	
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Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)

BSB	Michael D. Powell et al., "Reducing Set-Associative Cache Energy via Way-Prediction and Selective Direct-Mapping," Proceedings of the 34 th International Symposium on Microarchitecture (2001)				
EXAMINER	DATE CONSIDERED 2/7/5/4				
	DPCC 411109				

EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant

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^{*} Corresponds to U.S. Patent 6,335,718 listed above

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